

In the claims:

This listing of claims will replace all prior versions and listings of claims in the application:

- 1 1-10. (canceled).
- 1 11. (currently amended) The automated method of claim [[10]] 19 wherein the preferred gain of
2 the cells is determined using a continuous buffering assumption.
- 1 12. (currently amended) The automated method of claim [[2]] 19 wherein said initial delay
2 values are determined during library analysis.
- 1 13-18. (canceled)
- 1 19. (new) An automated method for designing an integrated circuit layout with a computer,
2 comprising:
 - 3 selecting cells from a cell library to implement a circuit path;
 - 4 prior to placement of the circuit path, determining initial delay values for the selected
5 cells based on corresponding preferred gains of the selected cells;
 - 6 prior to placement of the circuit path, determining an adjusted initial delay value for at
7 least one of the selected cells by performing at least one of:
 - 8 compressing the initial delay value of at least one of the selected cells to meet delay
9 constraints for the circuit path, and
 - 10 stretching the initial delay value of at least one of the selected cells to reduce slack in
11 the circuit path;
 - 12 performing a placement of the selected cells for the circuit path, including assigning wire
13 loads to the selected cells;
 - 14 adjusting size or area of one or more of the selected cells during or after placement in
15 response to the assigned wire loads, to maintain the initial delay value or the adjusted initial
16 delay value for the corresponding selected cells; and
 - 17 routing the selected cells for the circuit path.

1 20. (new) The automated method of claim 19, including prior to placement, inserting a
2 buffer in the circuit path when there is available slack in the circuit path.

1 21. (new) The automated method of claim 20, including prior to placement, determining
2 net weight values for the selected cells, the net weight values representing sensitivity of total area
3 of a circuit design to load on the corresponding cell, and determining whether to insert a buffer
4 on the output of a given cell in the selected cells using the net weight value of the given cell.

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